ABOUT THE ORGANIZATION

Andhra University was constituted in the year 1926. The 86-year old institution is fortunate to have Sir C.R. Reddy as its founder Vice-Chancellor, as the steps taken by this visionary proved to be fruitful in the long run. Former President of India Dr. Sarvepalli Radha Krishna was one of its Chancellors, who succeeded Dr. C. R. Reddy in 1931. The leaders of the university always believed that excellence in higher education is the best investment for the country and engaged the services of famous educationists to mention a few set high standards for teaching and research. Keeping in pace with the global needs and challenges under the leadership and guidance of successive Vice-Chancellors, the university is offering several new courses of relevance and contemporary significance. Andhra University has established an international image in academic excellence with accreditation by “NAAC” with “A” grade in April 2002 and is general university in the country to get ISO 9001: 2008 Certification in 2009. The university has five constituent colleges and four AU campuses. Presently it is among the top universities in India for engineering. Over the years, the Andhra University College of Engineering (A) grew from strength to strength and at present it has 15 departments offering UG, PG and Research programs.

ABOUT THE DEPARTMENT

The Department of Electrical Engineering was started in 1955 as of the three constituents of the Department of Engineering in Andhra University and grown to the status of full Department in 1969. Currently department offers a UG in Electrical and Electronics engineering and 3 PG programs. The Department also offers Ph.D. programme to promote basic research activities in the areas related to Electrical Engineering. The consultancy service is also rendered by the department. The Department has well established with conventional laboratories like Electrical Machines, Networks, Measurements, Power Electronics, Microprocessors, Control Systems and Digital Electronics. The faculty of the department is involved in research activities in the areas of fault detection, power system stability, load flows, optimal power flow, model reduction, decentralized fault detection, Electric Drives, Distribution system automation, Distributed Generation, FACTS devices, and synchrophasor applications to power systems.

OBJECTIVE:

The proposed program is organized with an aim to familiarize the faculty and student with the state-of-the-art technologies used in the real time operation and control of power systems, and also to familiarize the faculty of the various engineering colleges with the current research areas, to enable them to carry out active research to build their career in the area of power systems. It is expected that the lectures delivered by experienced speakers from IITs and industry will benefit the faculty and students in the future endeavors’.

TOPIC FOR DISCUSSION INCLUDE

1. Introduction to Synchrophasor technology.
2. Synchrophasor standards.
3. ATC assessment using synchrophasor measurements.
5. Power system optimization.
6. Special lecture on Indian Grid failure and synchrophasor initiatives.

WHO MAY BENEFIT

Working professionals, teachers, academicians and students working in the area of power systems are expected to benefit from this course.

RESOURCE PERSONS:

The faculty from reputed institutions like IITs, Industry, and AU will deliver the lectures in this work shop.

LECTURE NOTES:

Participants can be made available with the presentations and relevant material, related to the various topics covered during the workshop.

PARTICIPATION FEE:

Industries and Govt. Organizations : Rs 2000/-
Academic Institutes Faculty : Rs 1000/-
Full time scholars/PG Students : Rs 500/-

HOW TO APPLY:

Application in the attached form along with the DD drawn in the favour of “convener,SAPS-2014” Payable at Visakhapatnam, AP.

IMPORTANT DATES:

Last date for receiving registration form: 26 Jun., 2014
Intimation of Confirmation: Immediately after receiving the application subjected to the limit on the participants

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Call for Participation
A Two-Day Workshop on
SYNCHROPHASOR APPLICATIONS TO
POWER SYSTEMS
(SAPS – 2014)

Sponsored by
TEQIP-Phase II

Organized by
DEPARTMENT OF ELECTRICAL ENGINEERING
AU COLLEGE OF ENGINEERING (A)
ANDHRA UNIVERSITY
VISAKHAPATNAM-530003, A.P INDIA